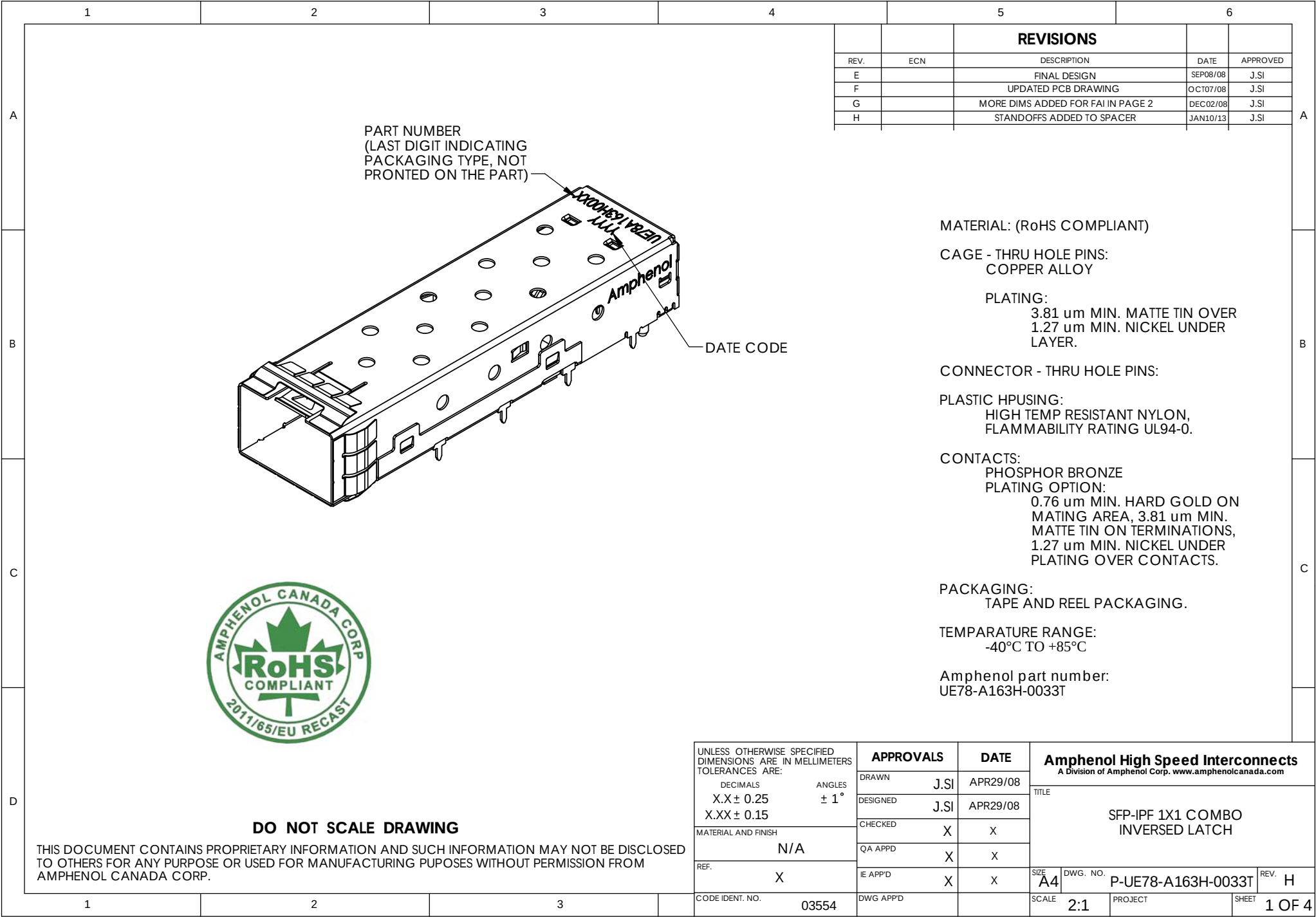
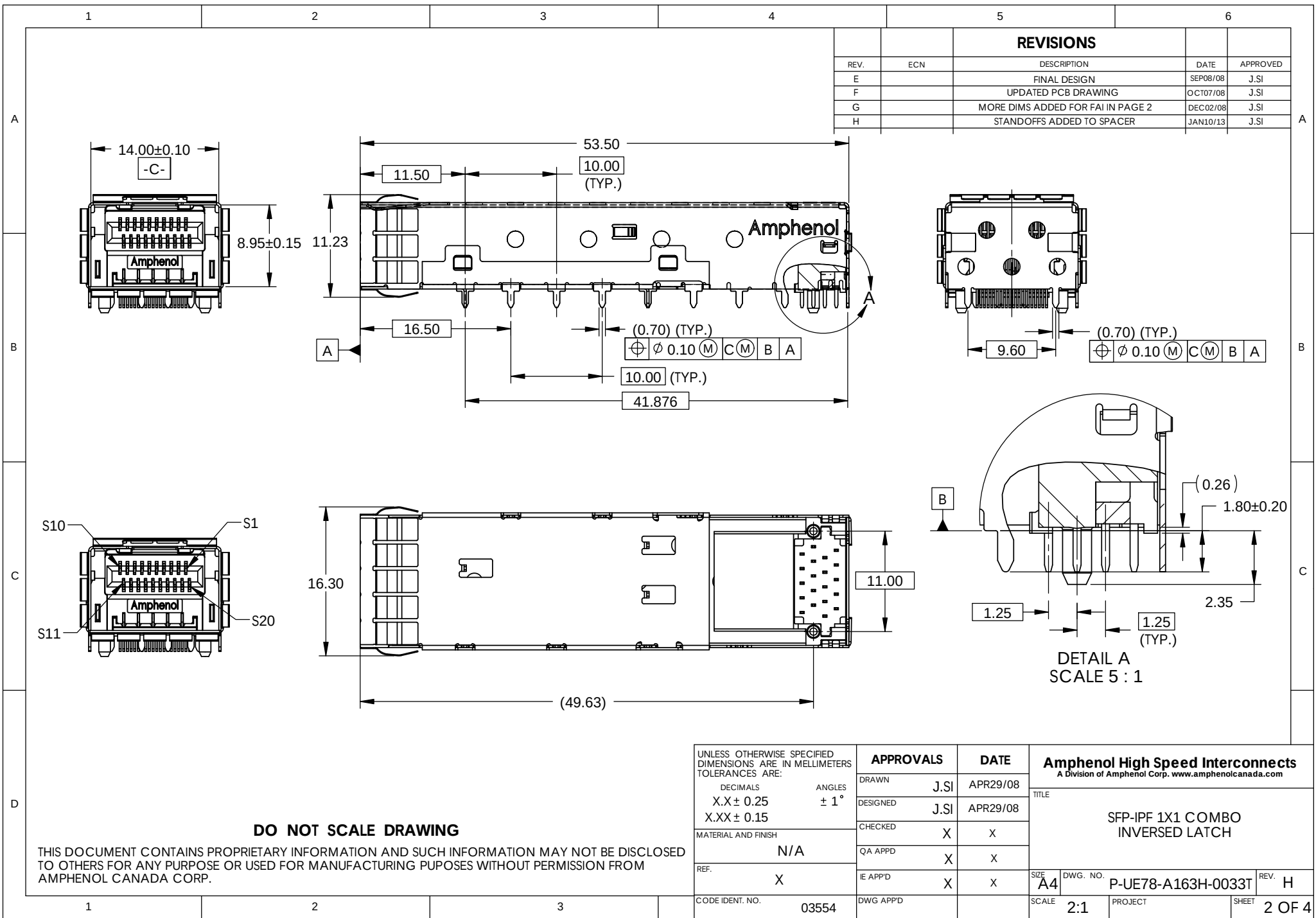
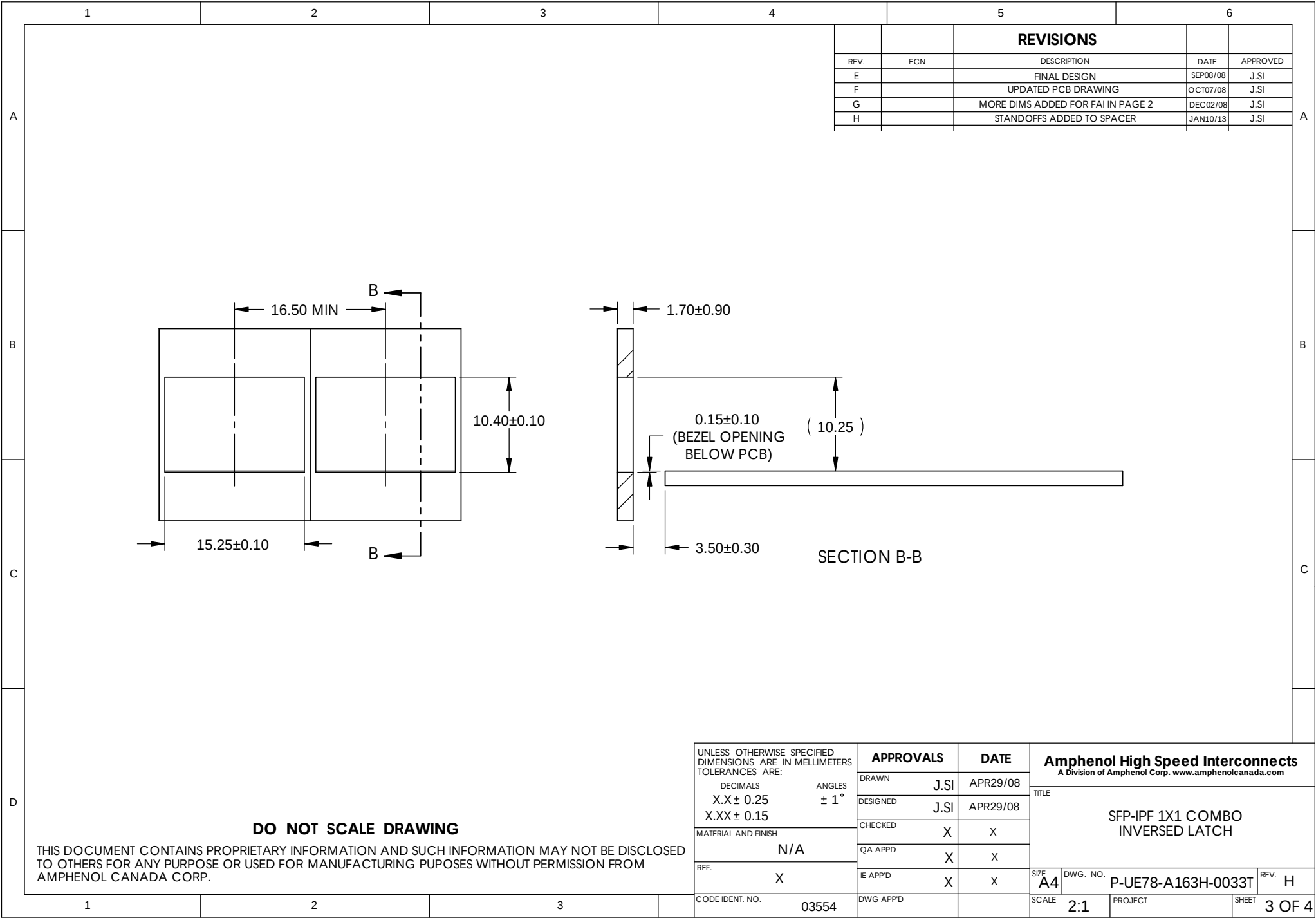






REVISIONS				
REV.	ECN	DESCRIPTION	DATE	APPROVED
E		FINAL DESIGN	SEP08/08	J.SI
F		UPDATED PCB DRAWING	OCT07/08	J.SI
G		MORE DIMS ADDED FOR FAI IN PAGE 2	DEC02/08	J.SI
H		STANDOFFS ADDED TO SPACER	JAN10/13	J.SI

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MELLIMETERS TOLERANCES ARE: DECIMALS ANGLES X.X ± 0.25 ± 1° X.XX ± 0.15		APPROVALS		DATE	Amphenol High Speed Interconnects A Division of Amphenol Corp. www.amphenolcanada.com TITLE SFP-IPF 1X1 COMBO INVERSED LATCH		
		DRAWN	J.SI	APR29/08			
		DESIGNED	J.SI	APR29/08			
		CHECKED	X	X			
MATERIAL AND FINISH N/A		QA APPD	X	X	SIZE A4		
REF. X		IE APP'D	X	X			
CODE IDENT. NO. 03554		DWG APP'D			SCALE 2:1	PROJECT	SHEET 2 OF 4

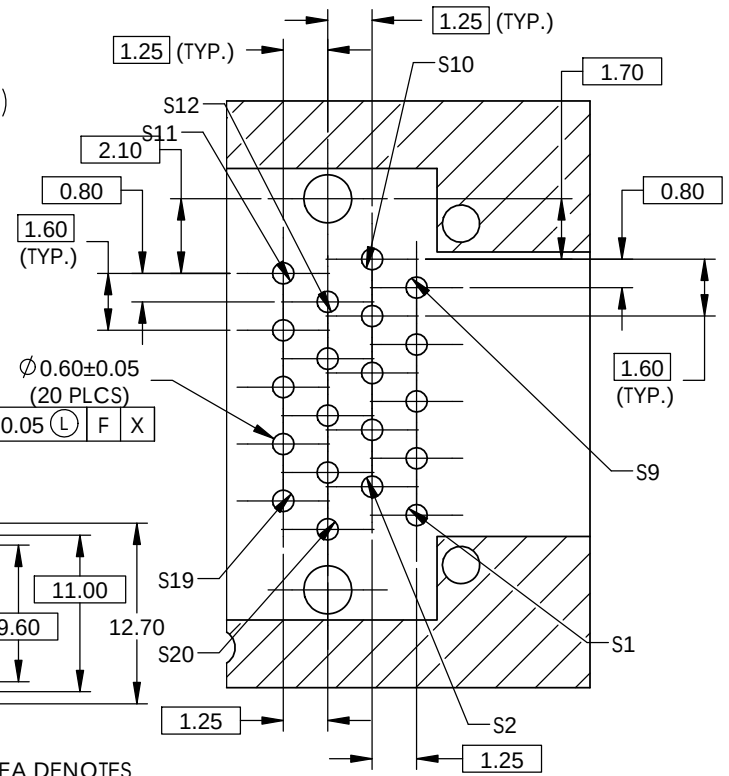
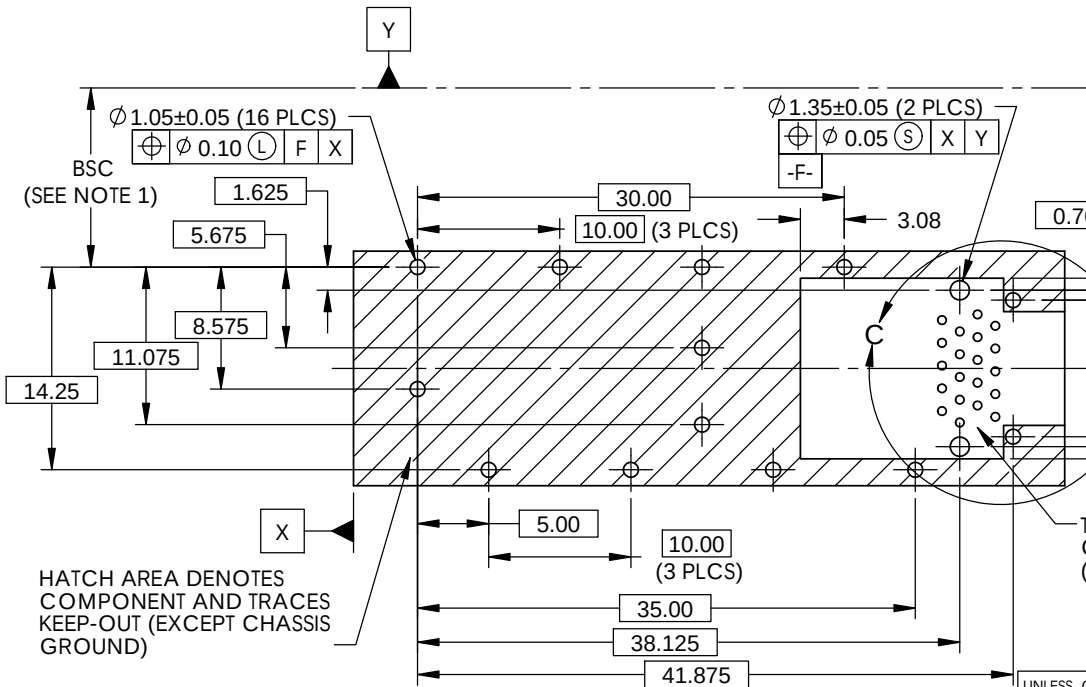
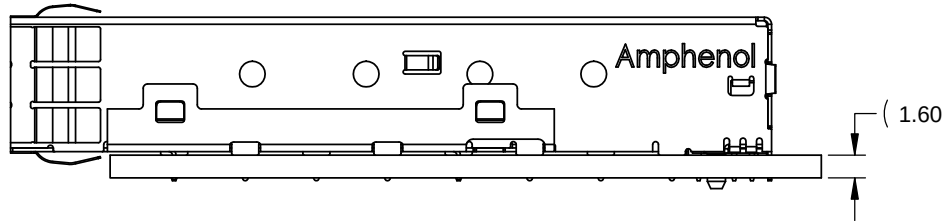


NOTES:

1. DATUM AND BSC DIMS ESTABLISHED BY CUSTOMER.
2. HOLES ARE CHASSIS GROUND, 16 PLACES.

REVISIONS

REV.	ECN	DESCRIPTION	DATE	APPROVED
E		FINAL DESIGN	SEP08/08	J.SI
F		UPDATED PCB DRAWING	OCT07/08	J.SI
G		MORE DIMS ADDED FOR FAI IN PAGE 2	DEC02/08	J.SI
H		STANDOFFS ADDED TO SPACER	JAN10/13	J.SI



DETAIL C
SCALE 5 : 1

THIS AREA DENOTES
COMPONENT KEET-OUT
(TRACES ALLOWED)

DO NOT SCALE DRAWING

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		DRAWN	J.SI	APR29/08		
DECIMALS	ANGLES	DESIGNED	J.SI	APR29/08	TITLE SFP-IPF 1X1 COMBO INVERSED LATCH	
X.X ± 0.25	± 1°	CHECKED	X	X		
X.XX ± 0.15		QA APPD	X	X		
MATERIAL AND FINISH	N/A	IE APPD	X	X	SIZE A4	DWG. NO. P-UE78-A163H-0033T
REF.	X	DWG APPD			SCALE 2:1	PROJECT
CODE IDENT. NO.	03554				SHEET	4 OF 4

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